

FIG . 1

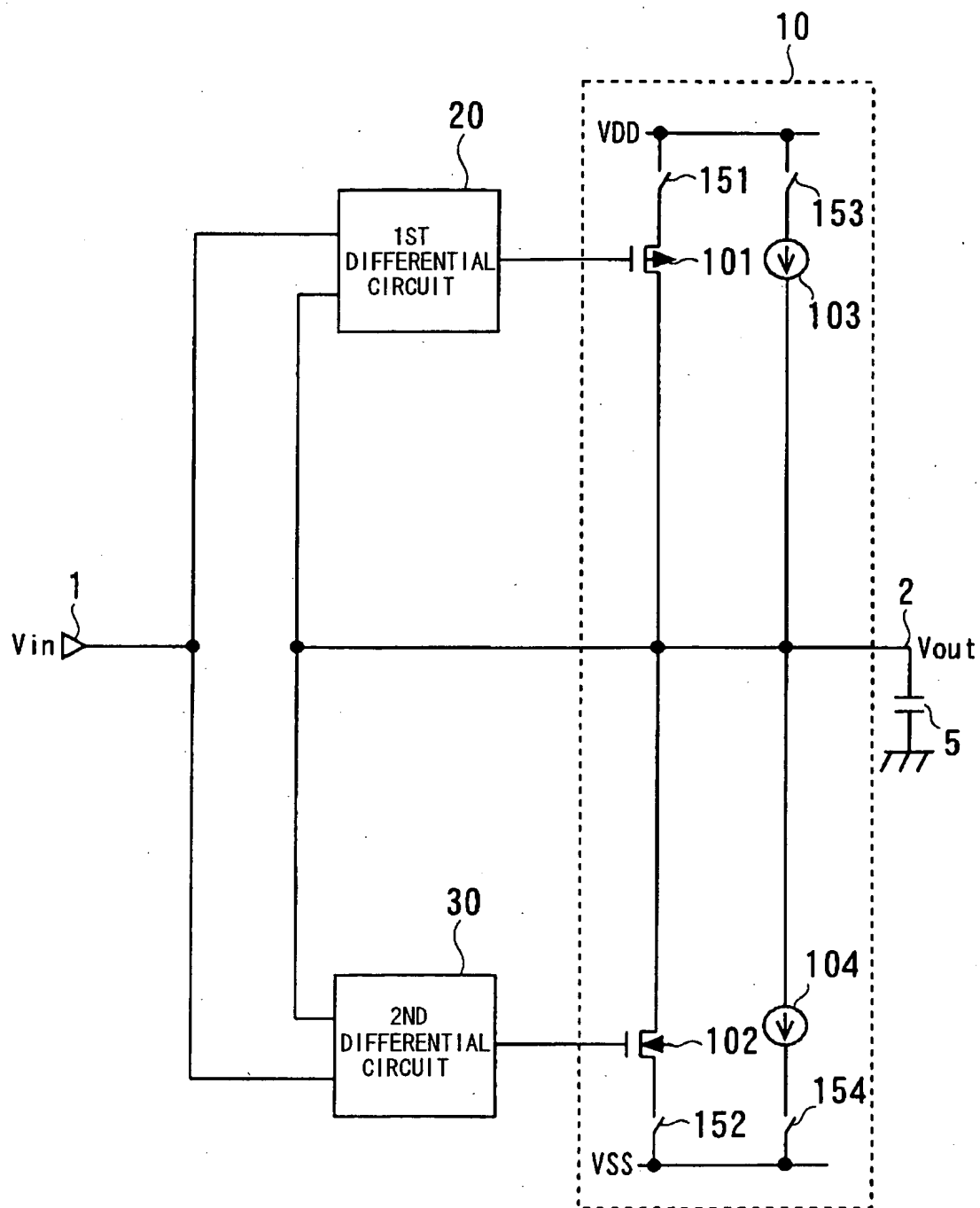


FIG . 2

DRIVING	DEVICE	1ST DATA DRIVING PERIOD		2ND DATA DRIVING PERIOD	
		1ST PERIOD	2ND PERIOD	1ST PERIOD	2ND PERIOD
CHARGING	TRANSISTOR 101	ACTIVE	ACTIVE	ACTIVE	INACTIVE
	CONSTANT CURRENT SOURCE 103	INACTIVE	INACTIVE	INACTIVE	ACTIVE
DISCHARGING	TRANSISTOR 102	ACTIVE	INACTIVE	ACTIVE	ACTIVE
	CONSTANT CURRENT SOURCE 104	INACTIVE	ACTIVE	INACTIVE	INACTIVE

FIG . 3A

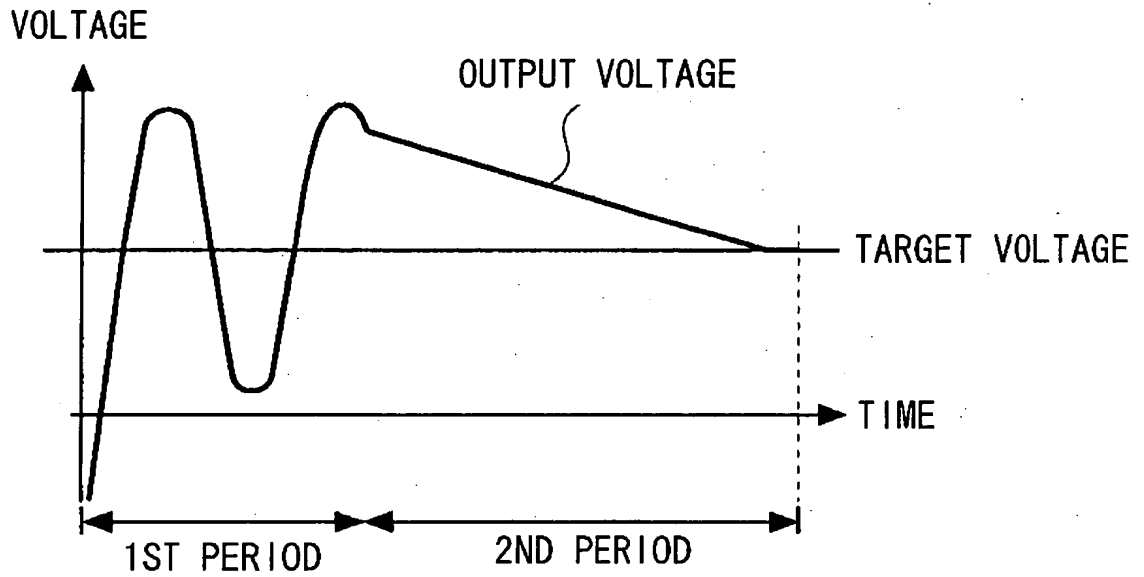


FIG . 3B

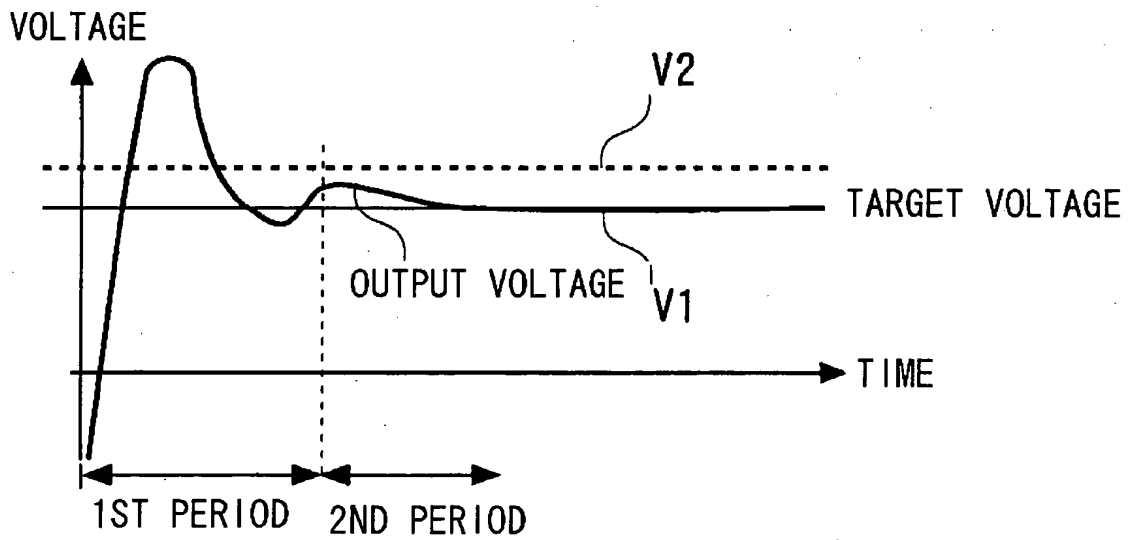


FIG . 4

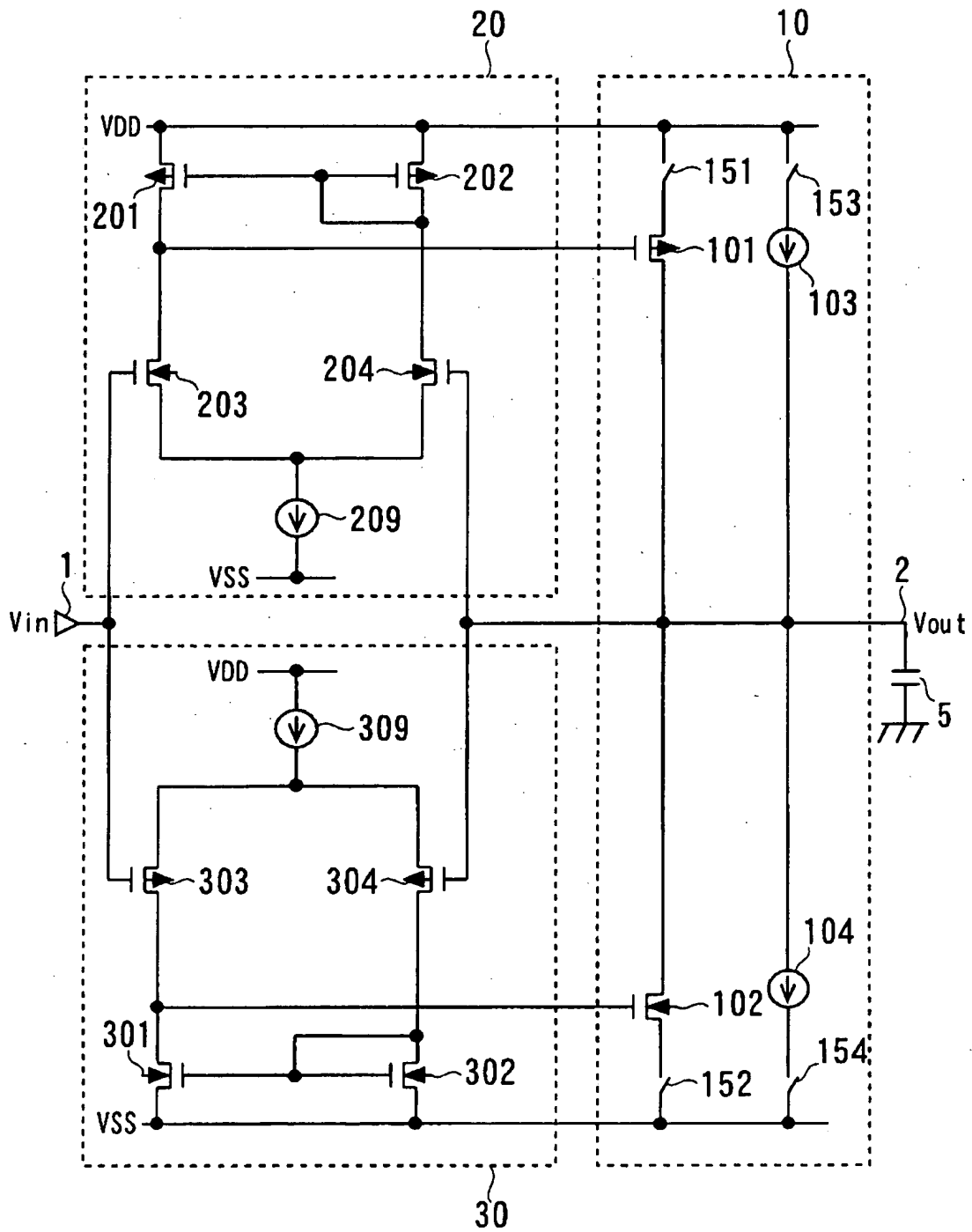


FIG . 5

	(Vth203, Vth204)	(Vth303, Vth304)	(Ids203, Ids204)	(Ids303, Ids304)
①	$V_{th203} > V_{th204}$	$V_{th303} = V_{th304}$	$I_{ds203} = I_{ds204}$	$I_{ds303} = I_{ds304}$
②	$V_{th203} = V_{th204}$	$V_{th303} < V_{th304}$	$I_{ds203} = I_{ds204}$	$I_{ds303} = I_{ds304}$
③	$V_{th203} = V_{th204}$	$V_{th303} = V_{th304}$	$I_{ds203} > I_{ds204}$	$I_{ds303} = I_{ds304}$
④	$V_{th203} = V_{th204}$	$V_{th303} = V_{th304}$	$I_{ds203} = I_{ds204}$	$I_{ds303} < I_{ds304}$

FIG . 6

CHARACTERISTICS OF n-CHANNEL TRANSISTOR 204

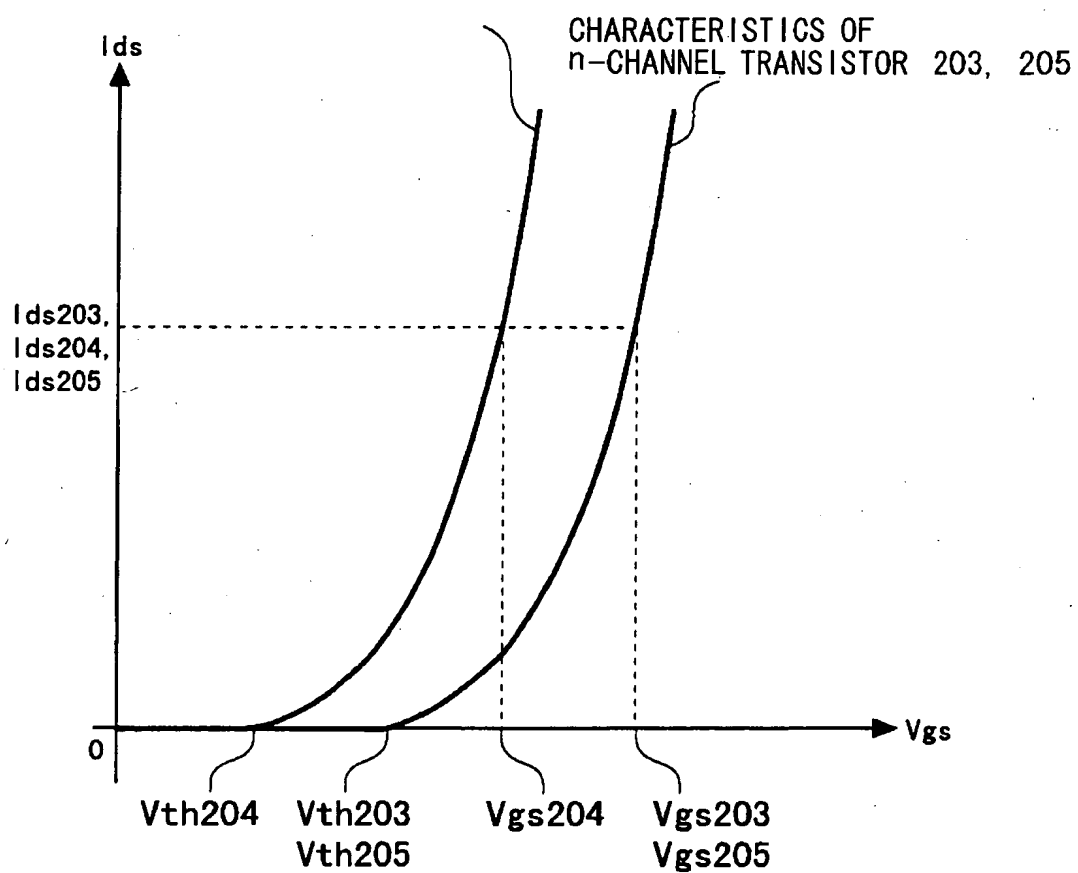


FIG . 7

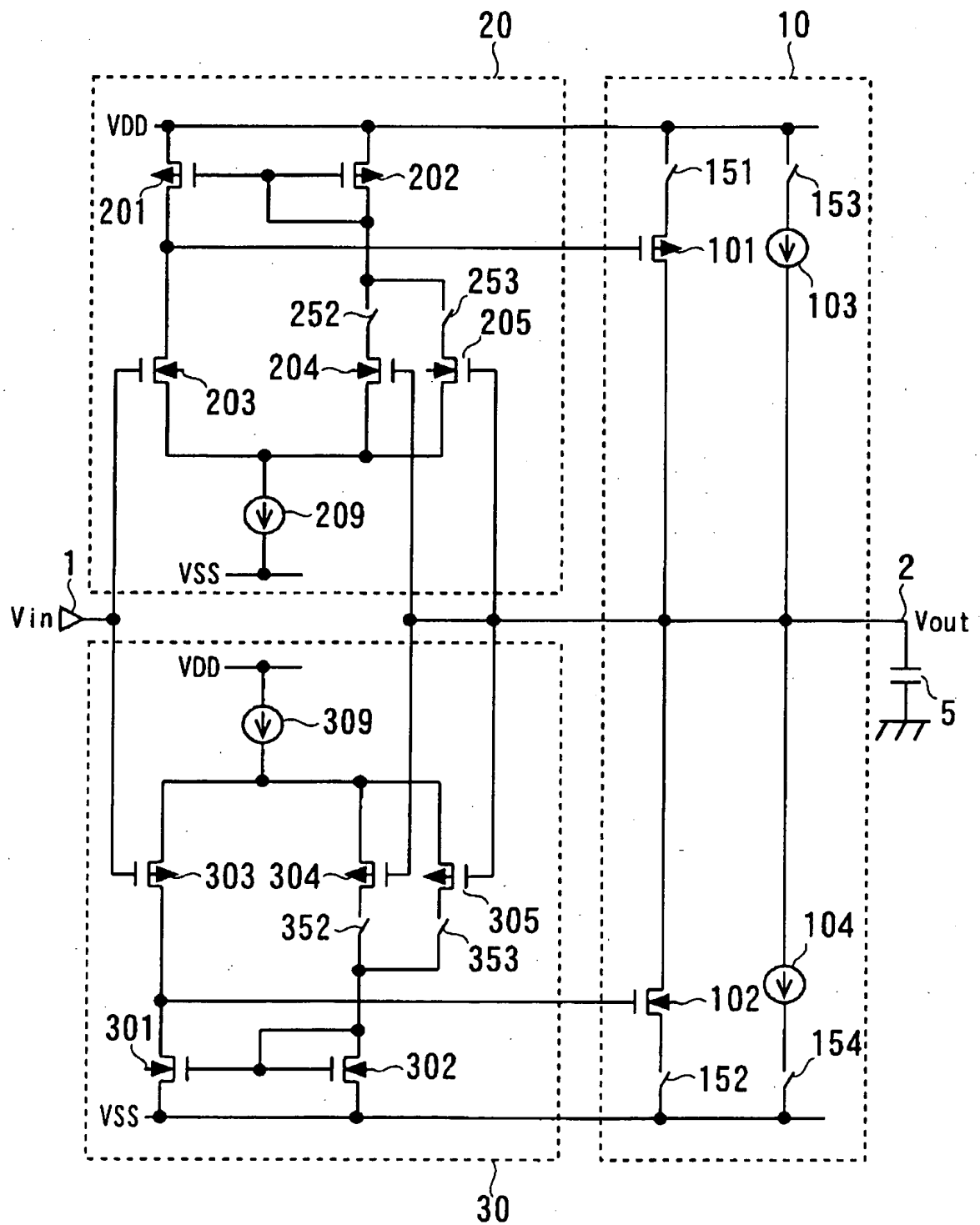


FIG . 8

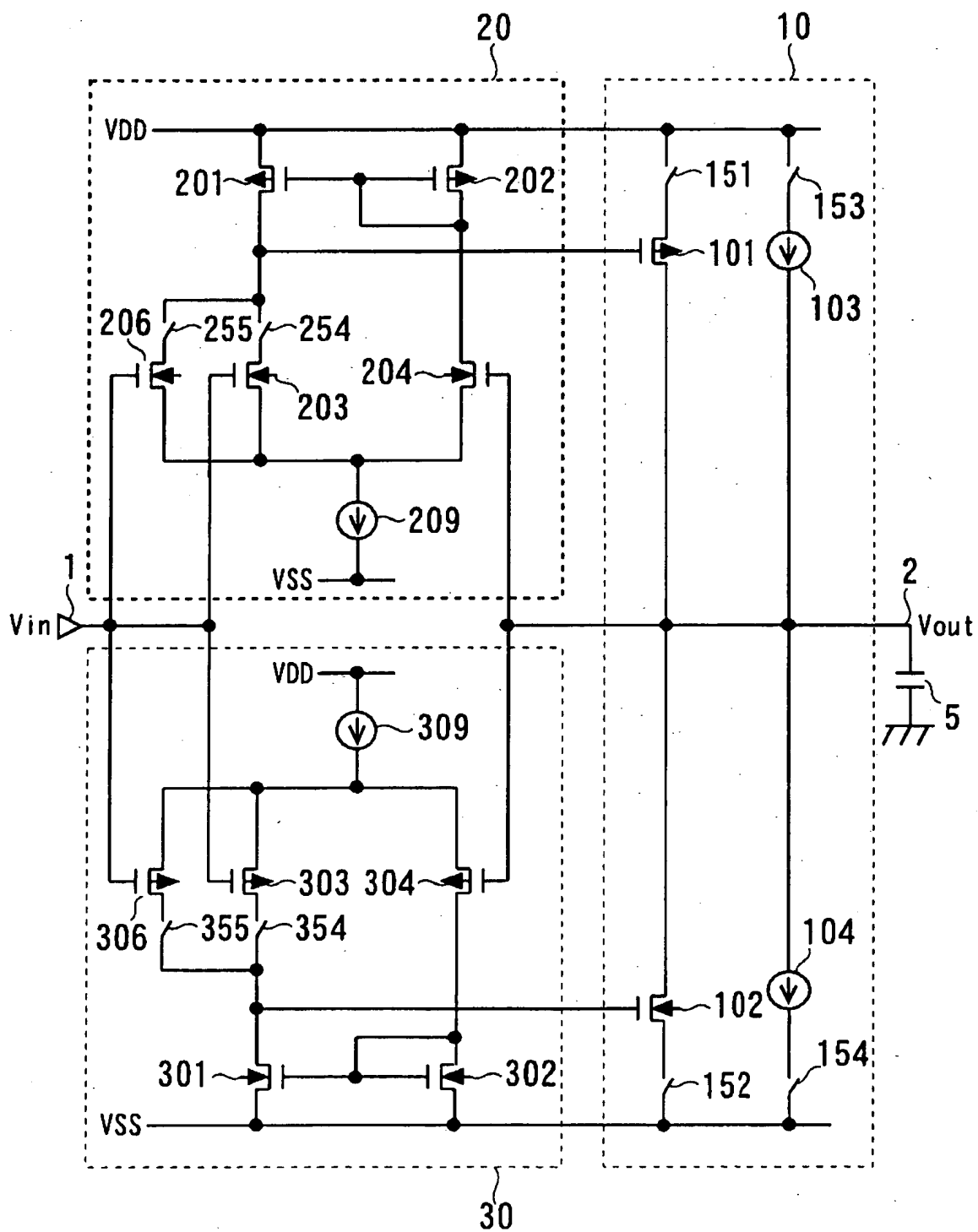


FIG. 9

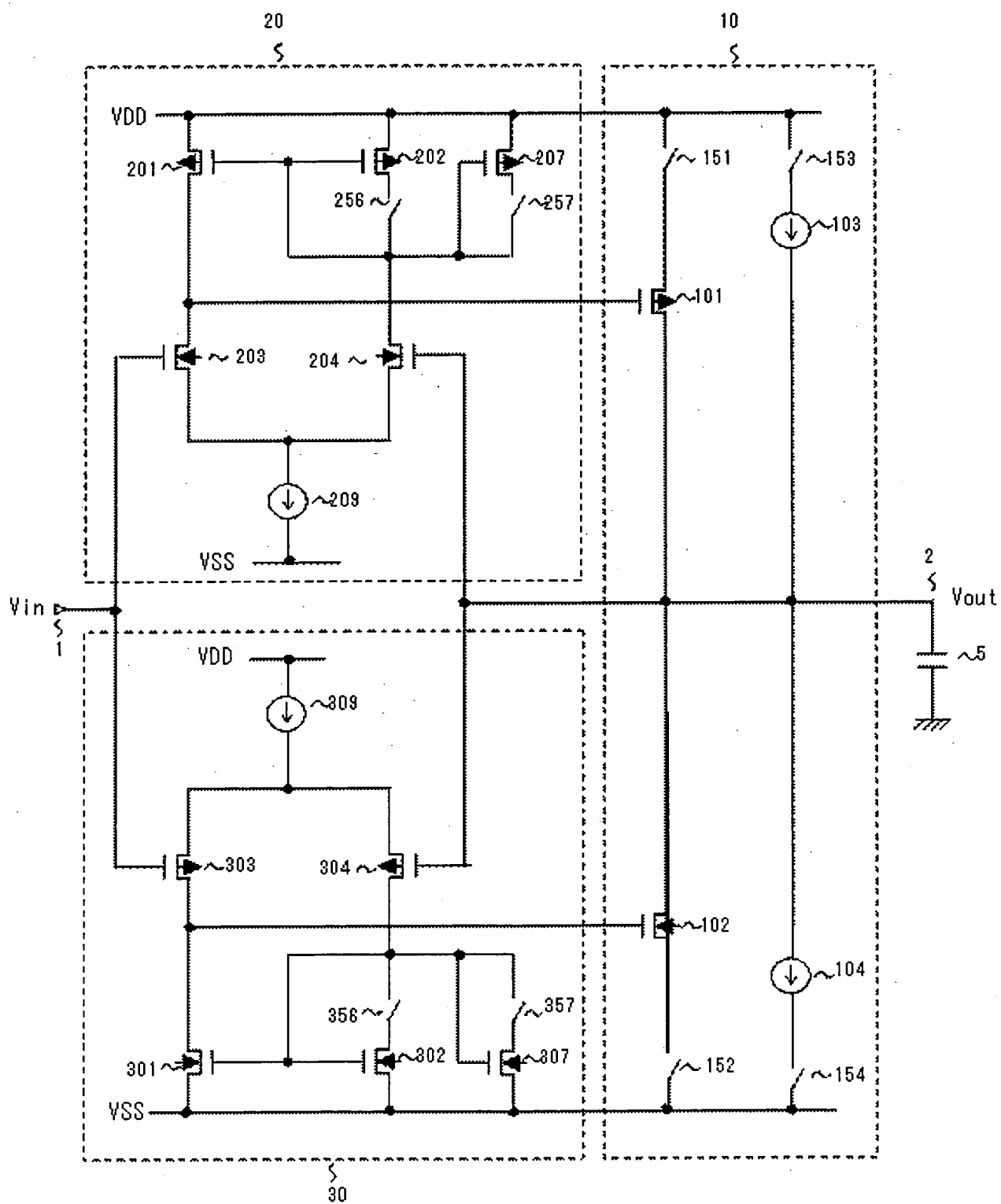


FIG . 10

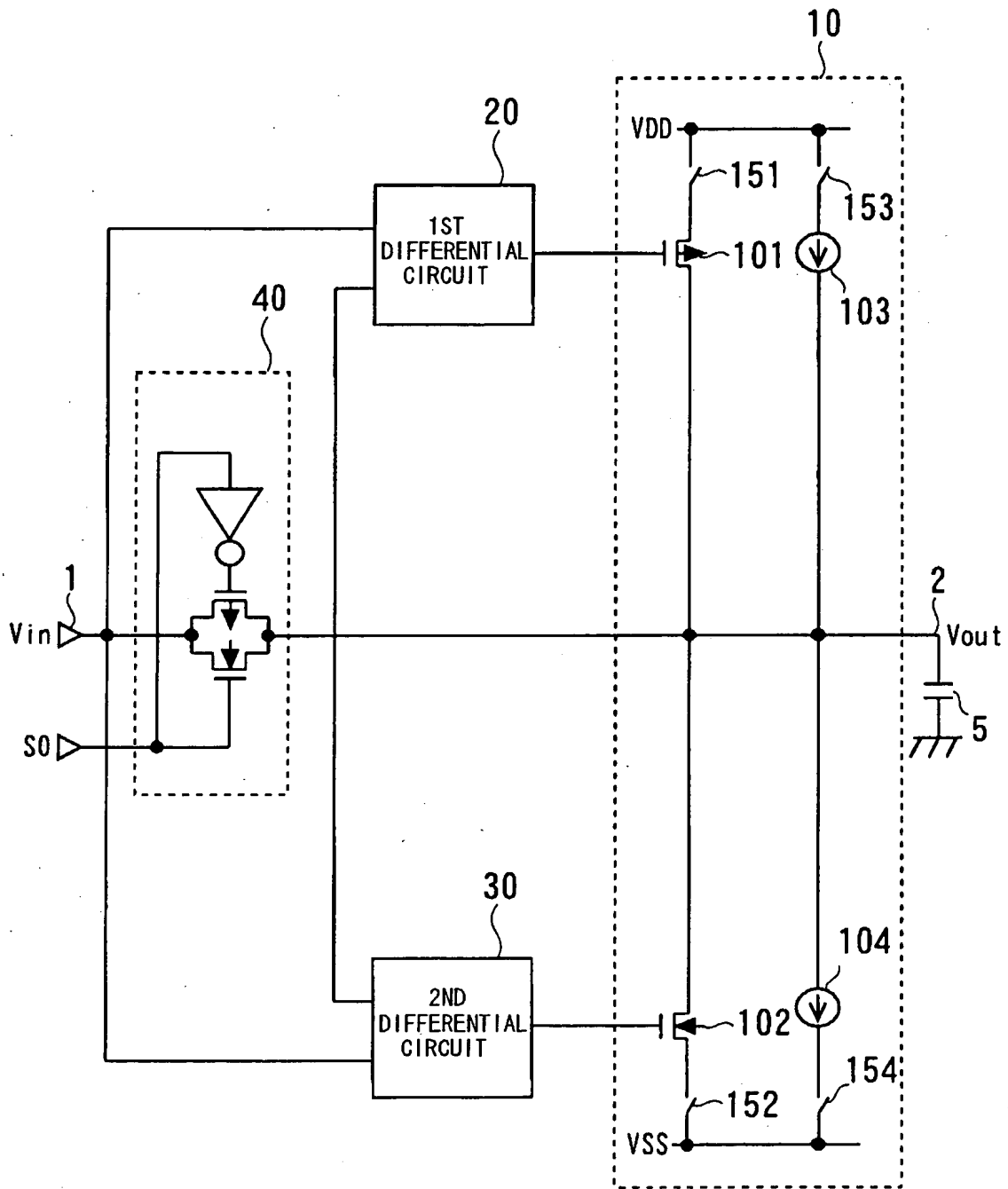


FIG . 11

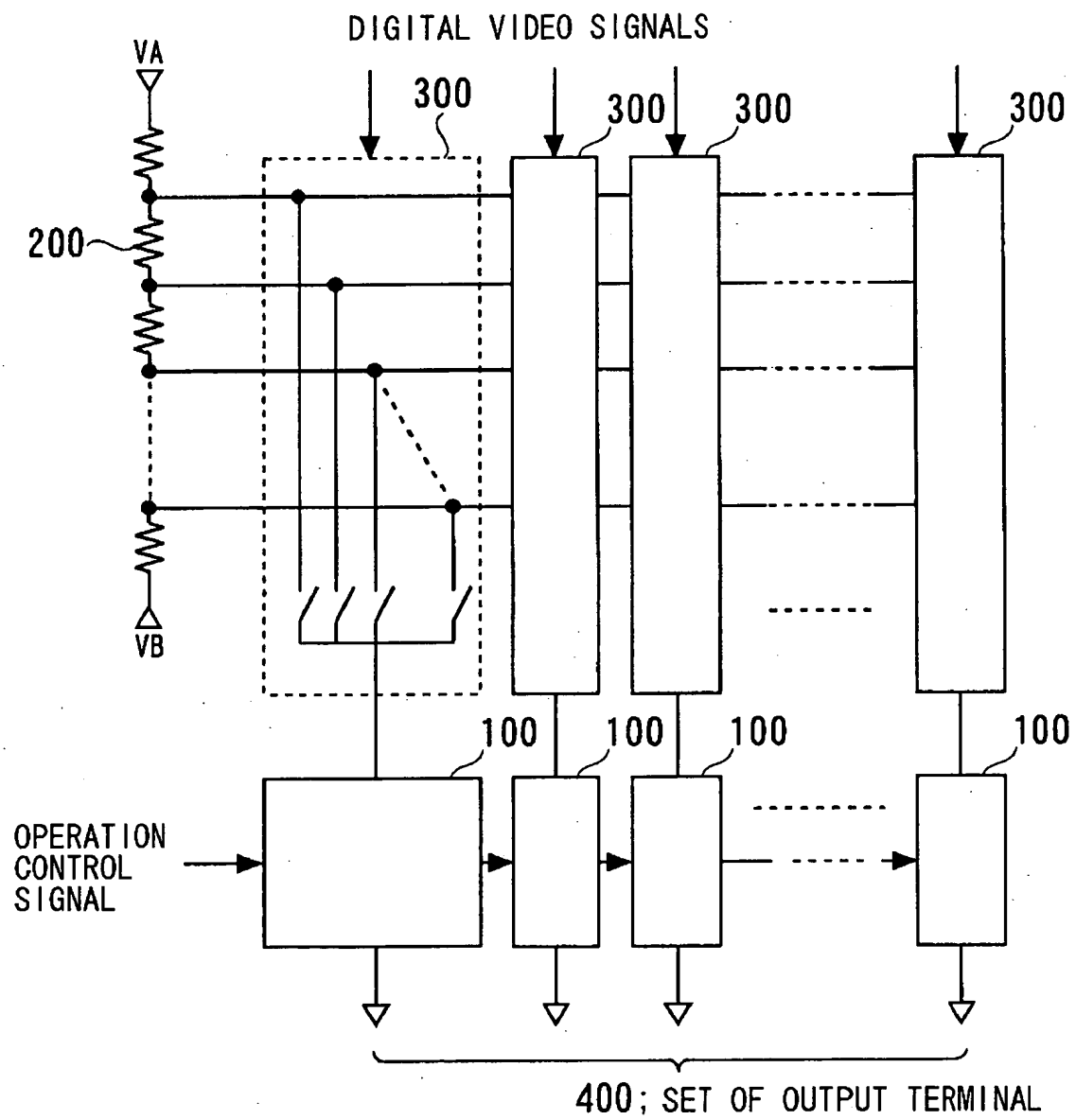


FIG . 12

PRIOR ART

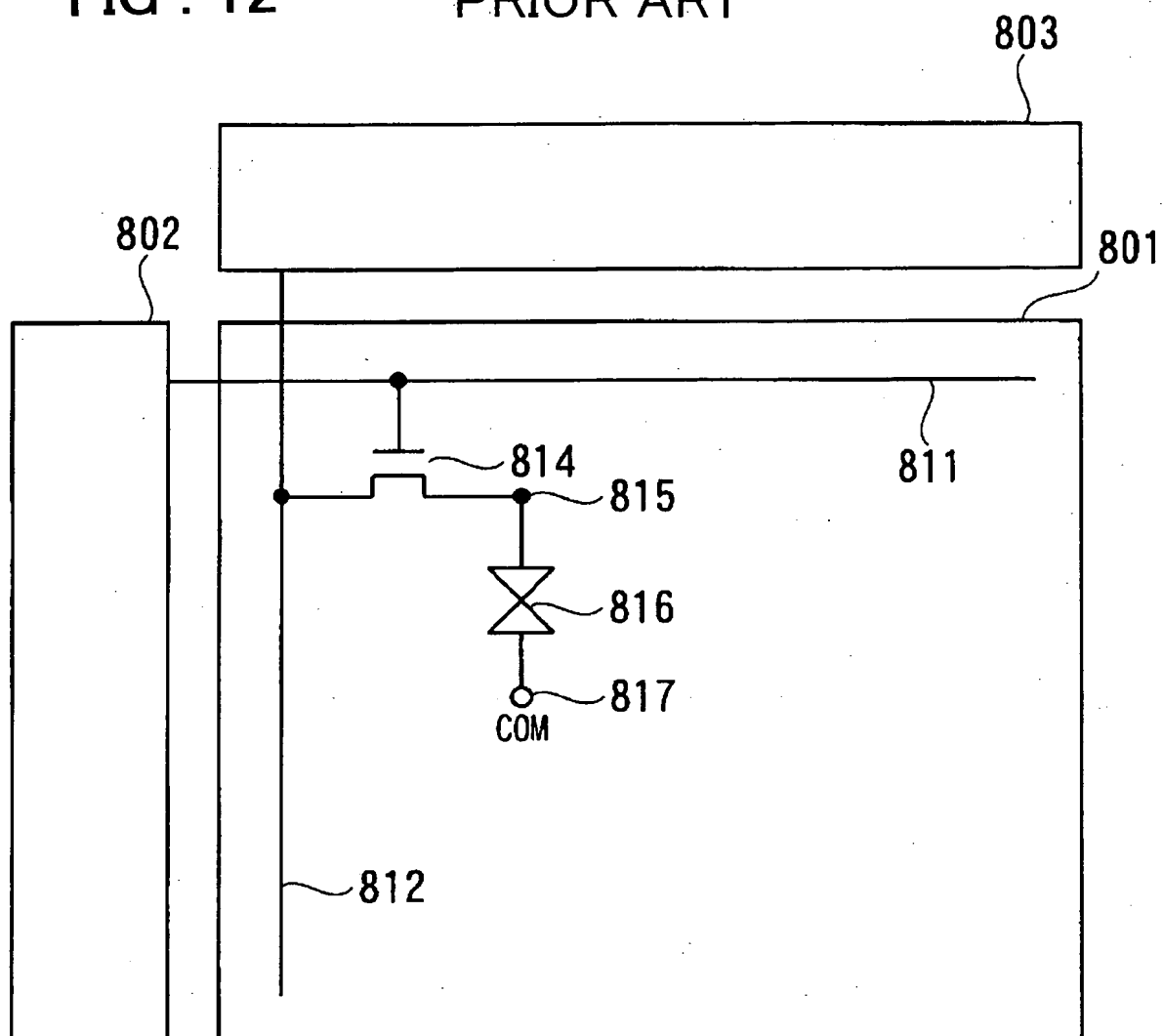


FIG . 13

PRIOR ART

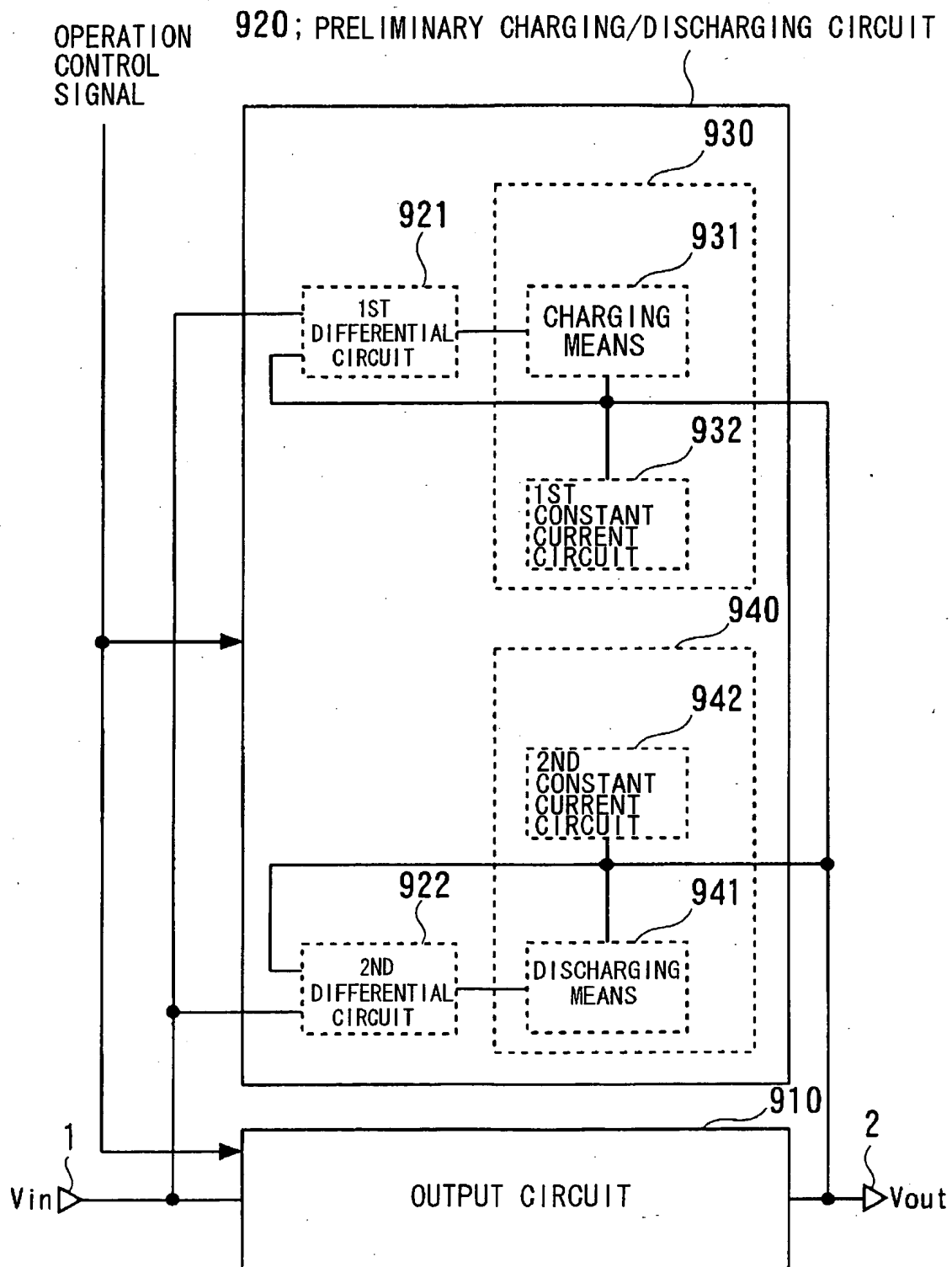


FIG. 14 PRIOR ART

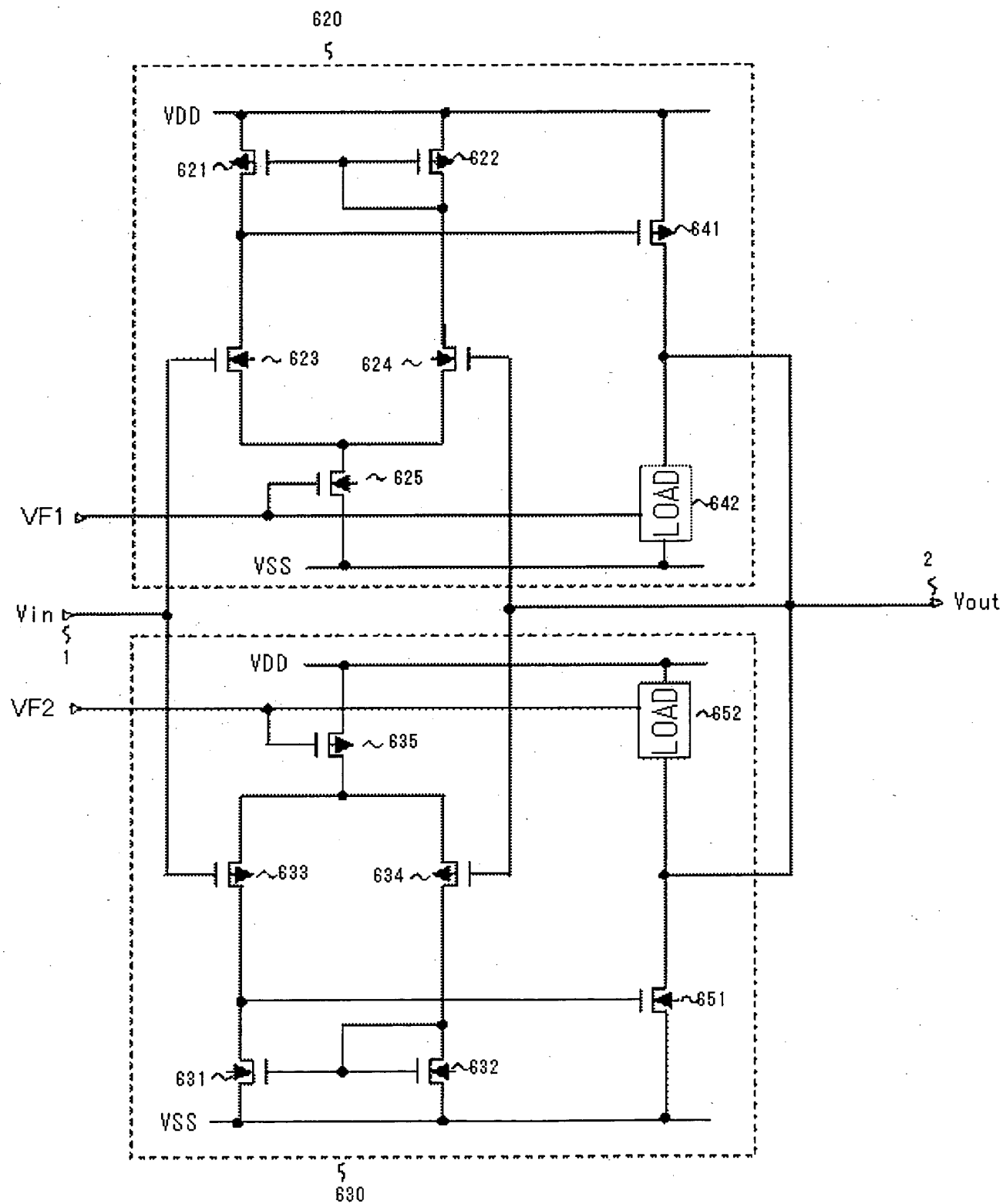


FIG. 15 PRIOR ART

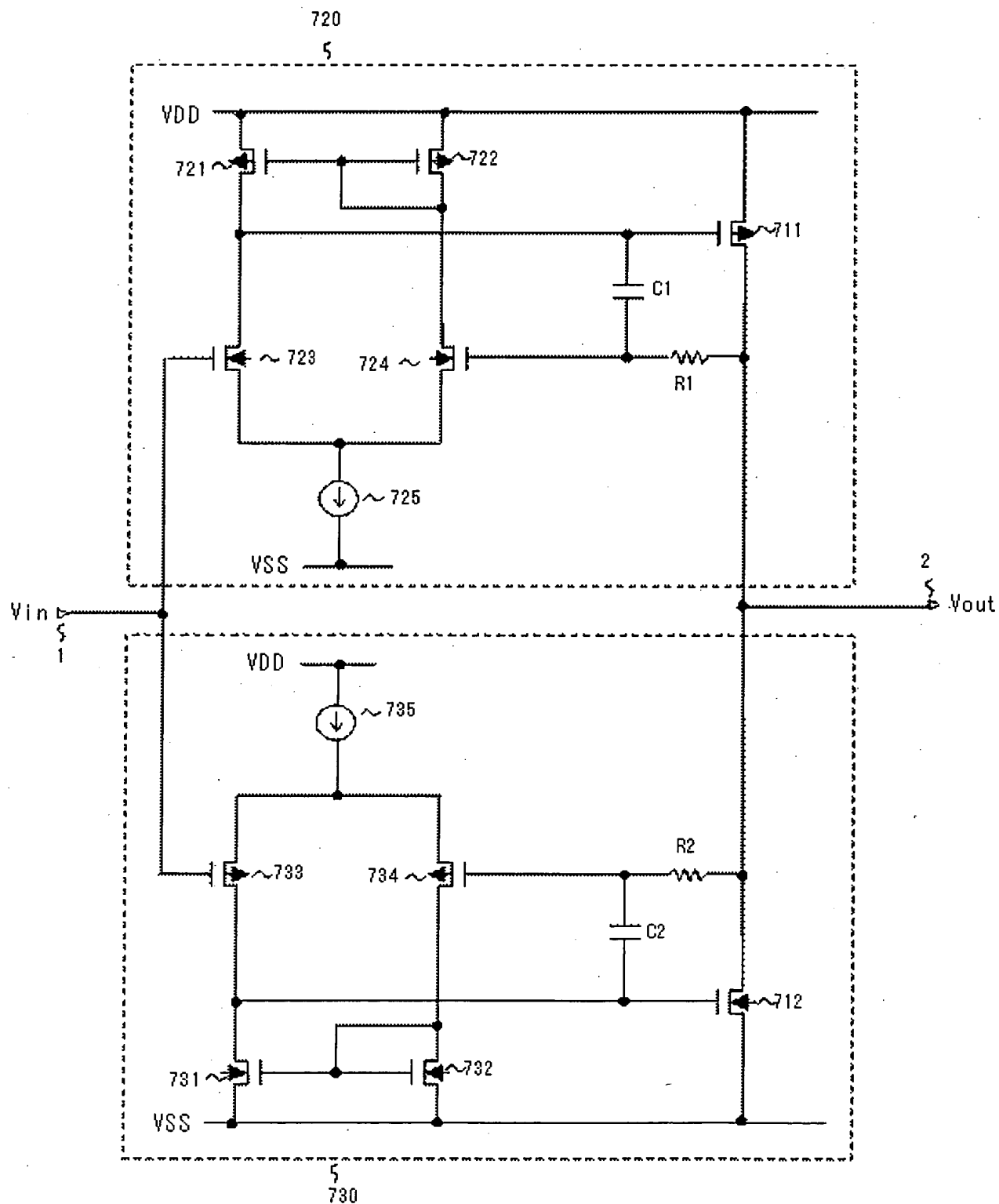


FIG. 16 PRIOR ART

